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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Pierre-Yvan LIARDET and William ORLANDO

Serial No:

Confirmation No:

Unassigned Unassigned Herewith

Filed: For:

SHARING OF A LOGIC OPERATOR HAVING A WORK

REGISTER

Examiner:

Unassigned

Art Unit:

Unassigned

## **Mail Stop Patent Application**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir/Madam:

## PRELIMINARY AMENDMENT

Prior to examination on the merits of the application, please amend the above-identified application as follows:

## **IN THE ABSTRACT**

Please amend the Abstract as shown below:

A circuit for calculating a discriminating function with successive iterations and with a work register on data divided into blocks, comprising including: a single operator in wired logic for executing the function; a plurality of work registers sharing said the operator; and an element for selecting one of the work registers to be associated with the operator.